

SSC8111GS6

P-Channel Enhancement Mode MOSFET

Features

V _{DS}	V_{GS}	R _{DS(ON)} Typ.	l _D
-16V	±12V	28mΩ@-4V5	-6A
		49mΩ@-2V5	-0/

> Description

This device is produced with high cell density DMOS trench technology, which is especially used to minimize on-state resistance. This device particularly suits low voltage applications such as portable equipment, power management and other battery powered circuits, and low in-line power dissipation are needed in a very small outline surface mount package.

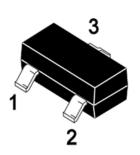
Applications

- TFT panel power switch
- Portable DVD, DPF
- High side DCDC converter
- High side driver for brushless DC motor

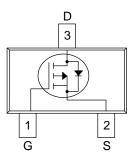
Ordering Information

Device	Package	Shipping	
SSC8111GS6	SOT-23	3000/Reel	

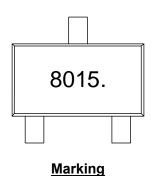
Pin configuration



SOT-23



Pin Configuration (Top View)





➤ Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit
V _{DSS}	Drain-to-Source Voltage	-16	V
V _{GSS}	Gate-to-Source Voltage	±12	V
I _D	Continuous Drain Current ^a	-6	Α
Ірм	Pulsed Drain Current ^b	-24	Α
P _D	Power Dissipation ^c	1.6	W
TJ	Operation junction temperature	-55~150	$^{\circ}$
T _{STG}	Storage temperature range	-55~150	$^{\circ}$

➤ Thermal Resistance Ratings (T_A=25°C unless otherwise noted)

Symbol	Parameter	Typical	Maximum	Unit
R _{θJA}	Junction-to-Ambient Thermal Resistance ^a	76	98	°C/W

Note:

- a. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with T_A =25°C. The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation P_D is based on $T_{J(MAX)}$ =150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.



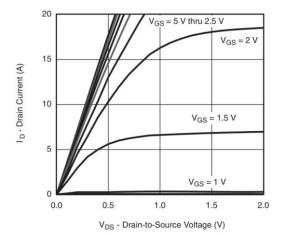


➤ Electrical Characteristics (T_A=25°C unless otherwise noted)

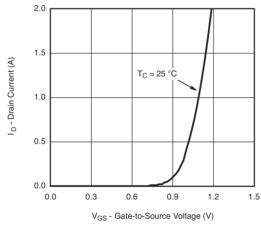
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = -250uA	-16			V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250uA$	-0.4	-0.7	-1.2	V
D : 0	R _{DS(on)}	V _{GS} = -4.5V, I _D = -6A		28	37	- mΩ
Drain-Source On-Resistance		V _{GS} = -2.5V, I _D = -4A		49	64	
Zero Gate Voltage Drain Current	IDSS	V _{DS} = -16V, V _{GS} = 0V			-1	μA
Gate-Source Leak Current	Igss	V _{GS} = ±12V, V _{DS} = 0V			±100	nA
Forward Voltage	V _{SD}	V _{GS} = 0V, I _S = -1.6A		-0.8	-1.3	٧
Input Capacitance	Cıss			643		pF
Output Capacitance	Coss	$V_{DS} = -8V$, $V_{GS} = 0V$, $f = 1MHz$		175		
Reverse Transfer Capacitance	Crss	I – IIVIDZ		154		
Turn-on Delay Time	T _{D(ON)}			13.2		
Rise Time	Tr	$V_{GS} = -4.5V$, $V_{DS} = -8V$,		10.5		
Turn-off Delay Time	T _{D(OFF)}	$R_L = 2\Omega$, $R_G = 3\Omega$		36		ns
Fall Time	T _f			24.5		
Total Gate Charge	Q _G	V _{GS} = -4.5V, V _{DS} = -8V,		12		nC
Gate to Source Charge	Q _{GS}			2		
Gate to Drain Charge	Q _{GD}	- I _D = -2A		3		



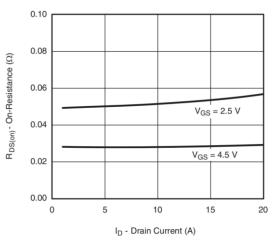
➤ Typical Performance Characteristics (T_A=25°C unless otherwise noted)



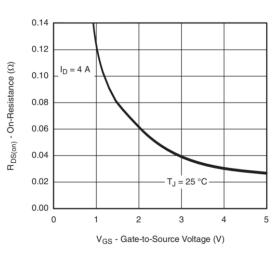
Output Characteristics



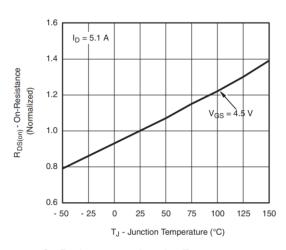
Transfer Characteristics



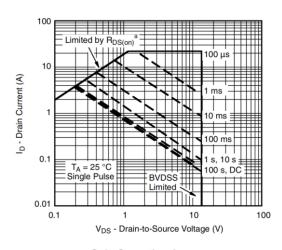
On-Resistance vs. Drain Current



On-Resistance vs. Gate-to-Source Voltage



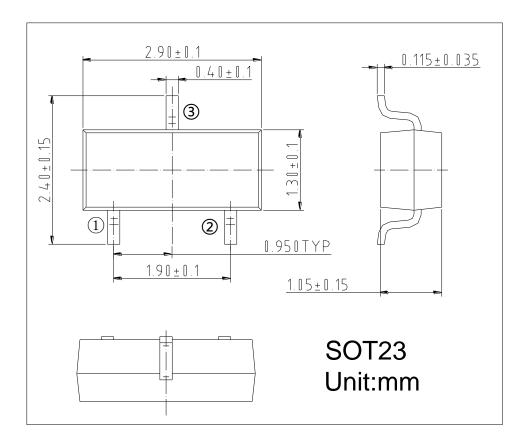
On-Resistance vs. Junction Temperature



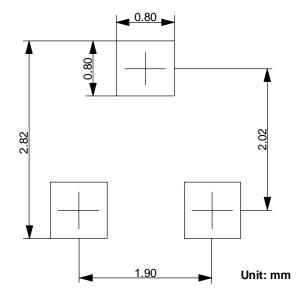
Safe Operating Area



> Package Information



> Suggested Pad Layout





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